

Attorney's Docket No.: 9269-

2131
#2
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Blaker et al.

Confirmation No.: 6250

Serial No.: 09/852,562

Group Art Unit: 2131

Filed: May 10, 2001

For: CRYPTOGRAPHIC DATA PROCESSING SYSTEMS, COMPUTER PROGRAM PRODUCTS, AND METHODS OF OPERATING SAME IN WHICH A SYSTEM MEMORY IS USED TO TRANSFER INFORMATION BETWEEN A HOST PROCESSOR AND AN ADJUNCT PROCESSOR

Date: March 5, 2002


Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a form PTO-1449, together with a copy of the identified document(s). This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(b), within three months of the filing date of the above-referenced application or before the mailing of a first Office Action on the merits, whichever event occurs last. Accordingly, no fee is required. The Commissioner is authorized to charge any additional fee, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,



D. Scott Moore
Registration No. 42,011

Customer Number:



20792

PATENT TRADEMARK OFFICE


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on March 5, 2002.



Traci A. Brown

Date of Signature: March 5, 2002

FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket Number 9269-4		Serial No. 09/852,562	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)							
				Applicants: Blaker et al			
				Filing Date: May 10, 2001		Group 2131	
U. S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1	5,329,623	7/12/94	Smith et al.	395	275	RECEIVED MAR 21 2002 Technology Center 2100
	2	5,961,626	10/5/99	Harrison et al.	710	129	
	3	6,081,895	6/27/00	Harrison et al.	713	189	
	4	6,219,789B1	4/17/01	Little et al.	713	200	
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	5	Kent et al. <i>Security Architecture for the Internet Protocol</i> . November 1998, pp.1-66.					
	6	Hifn 6500 Public Key Processor. http://www.hifn.com/products/6500.html , printed April 29, 2001.					
	7	FastMap Integrated Circuit. Rainbow Technologies Internet Security Group. October 1, 1998.					
	8	Preuss, Lisa. "Rainbow Technologies Announces OEM Availability of FastMap High Performance Public Key Integrated Circuit Processor," News Release. Atlanta, GA, October 21, 1998.					
	9	SafeNet: OEM Solutions. www.safenet-inc.com/technology/chips/Chip2141.asp , printed April 29, 2001.					
	10	Suchmann, David. <i>Electronic Products: Novel Approach to Chip Design Improves SSL Encryption</i> . September 3, 2001.					
	11	NetOctave Announces SSL and IPSec Security Accelerator Boards. News Release, September 11, 2001.					
	12	Next-generation Applications Need IPSec Security: NetOctave IPSec Solutions. Brochure, March, 2001.					
	13	Next-generation Applications Need SSL Security: NetOctave SSL Solutions. Brochure, March, 2001.					

EXAMINER
*EXAMINER

DATE CONSIDERED

Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.